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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q57908

Takao TOI

Appln. No.: 09/505,429

Group Art Unit: 2623

Confirmation No.: 7134

Examiner: Colin M. LaRose

Filed: February 16, 2000

For: IMAGE PROCESSING SYSTEM

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AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated December 17, 2002, please amend the above-identified application as follows:

IN THE CLAIMS:

The claims are amended as follows:

1. (Amended) An image processing system provided with a field programmable gate array which is capable of altering an internal logic description, said description prescribing operation during an operating state, wherein an image processing method of said image processing system comprises:

executing digital image processing of an interval of active pixel in the condition that a first internal logic description is written in said field programmable gate array;